Processor Architecture

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Book Condition: New. Publisher/Verlag: Springer, Berlin | From Dataflow to Superscalar and Beyond | Die Autoren beschreiben die Prinzipien der Prozessor-Architektur. Die verschiedenen Prozessortypen werden anhand von Beispielen ausführlich dargestellt. Die Vorstellung neuester Techniken und Implementierungsmethoden für künftige Mikroprozessorarchitekturen runden das Buch ab. This monograph surveys architectural mechanisms and implementation techniques for exploiting fine-grained and coarse-grained parallelism within microprocessors. It starts with a review of past techniques, continues with a comprehensive account of state-of-the-art techniques used in microprocessors that covers both the concepts involved and implementations in sample processors, and ends with a thorough review of the research techniques that will lead to future microprocessors. | A survey of architectural mechanisms and implementation techniques for exploiting fine- and coarse-grained parallelism within microprocessors. Beginning with a review of past techniques, the monograph provides a comprehensive account of state-of-the-art techniques used in microprocessors, covering both the concepts involved and implementations in sample processors. The whole is rounded off with a thorough review of the research techniques that will lead to future microprocessors. XXXXXXX Neuer Text This monograph surveys architectural mechanisms and implementation techniques for exploiting fine-grained and coarse-grained parallelism within microprocessors. It presents a comprehensive account of state-of-the-art techniques used in microprocessors...

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